**Verilog Format to Spice Format Translation**

The NetTran utility of the Synopsys Hercules tool can be used in order to translate a “**Verilog-Based Synthesized**” netlist to a “**Spice-Based**” netlist. The command for this translation is according to the following:

**Option 1**: nettran -verilog File\_Name.v -outType spice -outName File\_Name.sp

**Option 2**: nettran -verilog File\_Name.v -outName File\_Name.sp -verilog-b1 VDD -verilog-b0 GND -outType spice